	Туре	L #	Hits	Search Text			
1	BRS	L1		(crystal or piezo or piezoelectric) and (substrate or chip or 'ic') and recess and hermet\$6 and temperature and (varactor or capacit\$4) and (cas\$3 or hous\$3 or shield)			
2	BRS	L2		(crystal or piezo or piezoelectric) and (substrate or chip or 'ic') and recess and hermet\$6 and temperature and (varactor or capacit\$4) and (cas\$3 or hous\$3 or shield)			

					DBs		
1	·	USPAT					
2		US-PGPUB;	USOCR;	EPO;	JPO;	DERWENT;	IBM_TDB